

Monday September 11, 2023 — Tutorials (T) and Workshops (W), Dissemination Events (D), and Circuit Insights

ROOM	Auditorium III	1.04	5A	5B	5C	1.05	1.06	1.07	1.08	Auditorium IV
09:30 - 10:00	Registration									
10:00 - 12:30	T1 Radiation Hardening Techniques for Space Grade Ics	T2 The Pavlin Cell: A Versatile Analog Building Block for Power Conversion Applications	T3 Open-Source Neuromorphic Circuit Design Overview, Trends, and Opportunities	W5 EUROPRACTICE Workshop Design IP Sharing and Chiplets	T5 State-of-the-Art Methodologies for Modeling, Design, Simulation and Debug of Analog/Mixed-Signal Circuits	T6 Power Delivery Network Optimization for 5G Applications	W1 Technologies and Circuits for 5G Evolution to 6G	W2 Multi-Gigabit Optical Communications for Automotive	D1 Emulating, Listening and Communicating with Neurons	Circuit Insights
12:30 - 13:30	LUNCH BREAK									
13:30 - 15:30	T4 Continuous-Time Pipeline ADCs: From Fundamentals to Practical Implementations	D4 European Strengths and Gaps in Emerging Semiconductor Technologies	D2 Technologies, Devices, Circuits, and Algorithms for Neuromorphic Event-Based Vision System	W3 FinFETS from Cryo-CMOS to AI	W4 Towards Self Contained Recognition Integrated Classifiers for Smart Sensors	W6 Source-Gated Transistors: The Building Block of Next-Generation Thin-Film Edge Processing Systems	D3 Technologies Enabling Future Mobile Connectivity & Sensing	W2 Multi-Gigabit Optical Communications for Automotive	D1 Emulating, Listening and Communicating with Neurons	Circuit Insights
15:30 - 16:00	COFFEE BREAK									
16:00 - 18:00	W7 MOS-AK Workshop	D4 European Strengths and Gaps in Emerging Semiconductor Technologies	D2 Technologies, Devices, Circuits, and Algorithms for Neuromorphic Event-Based Vision System	W3 FinFETS from Cryo-CMOS to AI	W4 Towards Self Contained Recognition Integrated Classifiers for Smart Sensors	W6 Source-Gated Transistors: The Building Block of Next-Generation Thin-Film Edge Processing Systems	D3 Technologies Enabling Future Mobile Connectivity & Sensing	W2 Multi-Gigabit Optical Communications for Automotive	D1 Emulating, Listening and Communicating with Neurons	Circuit Insights