

Wednesday, September 13, 2023

ROOM	Auditorium II	Auditorium III	Auditorium IV	5A	5B	5C
08:30 - 09:10	<p>B1L-0 (Auditorium I) Plenary Keynote 3: Eric Yang The Heart of Artificial Intelligence Advanced Power Semiconductors Enable Greener, Denser, and Smarter Datacenters</p>					
09:10 - 09:20	changing room					
09:20 - 10:40	B2L-1 Boosting SAR A/D Converters	B2L-2 Compact Modeling	B2L-3 Biosensing & Photodetectors	B2L-4 Cryogenic Circuits & Systems for Quantum Computing	B2L-5 Wireline Techniques	B2L-6 Communication & Cryptography Circuits
10:40 - 11:10	COFFEE BREAK					
11:10 - 12:50	B3L-1 Analog Techniques	B3L-2 Numerical & Statistical Modeling	B3L-3 Digital & Analog AI Processors	B3L-4 Power Techniques & LDO	B3L-5 Frequency Synthesizers	
12:50 - 14:20	LUNCH BREAK					
14:20 - 15:00	<p>B4L-0 (Auditorium I) ESSCIRC Keynote 2: Brendan Farley Software Defined Radio for Next Generation Communication Networks</p>					
15:00 - 15:10	changing room					
15:10 - 16:30	B5L-1 Novel Hybrid A/D Converters	B5L-2 Memory Device Modeling	B5L-3 Biosensors	B5L-4 Power for Medical & IoT	B5L-5 RF Si Device Technology	
16:30 - 17:00	COFFEE BREAK					
17:00 - 17:40	<p>B6L-0 (Auditorium I) ESSDERC Keynote 2: Pedro Barquinha Oxide Thin-Film Transistors: Are we Reinventing Electronics? Or Dressing an Old Story with Today's Clothes?</p>					
17:40 - 17:50	changing room					
17:50 - 18:50	<p>EU CHIPS ACT Panel (Auditorium I)</p>					
19:30	GALA DINNER					
23:00	XABREGAS PALACE					