

PROGRAM AT GLANCE

Thursday, September 14, 2023

Room	Auditorium II	Auditorium III	Auditorium IV	5A	5B	5C
08:30 - 09:10	<p style="text-align: center;">C1L-0 (Auditorium I) ESSCIRC Keynote 3: Ángel Rodríguez-Vázquez Chip Architectures for Efficient Analog-to-Information Image Analysis using Out-of-the-Box Processing Concepts</p>					
09:10 - 09:20	Awards Ceremony - Auditorium I					
09:20 - 09:30	Presentation on ESSERC 2024 - Auditorium I					
09:30 - 09:40	changing room					
09:40 - 10:40	C2L-1 Innovations in High-Speed A/D Converters	C2L-2 Application Specific SoCs	C2L-3 Biomedical & Wearable Sensors	C2L-4 VCSEL Transmitters	C2L-5 Advanced CMOS LNA Techniques	C2L-6 Memory 2
10:40 - 11:10	COFFEE BREAK					
11:10 - 12:50	C3L-1 Advances in Delta-Sigma Converters	C3L-2 A-SSCC Special Session	C3L-3 SRAM Digital Computing in Memory	C3L-4 DC-DC Converters	BC3L-5 mm-Wave Phased Array Techniques	
12:50 - 14:20	LUNCH BREAK					
14:20 - 15:00	<p style="text-align: center;">C4L-0 (Auditorium I) ESSDERC Keynote 3: Kenneth O Silicon Technology Innovation Opportunities for Applications at 0.1 to 1 THz Beyond that for Transistors</p>					
15:00 - 15:40	<p style="text-align: center;">C5L-0 (Auditorium I) ESSCIRC Keynote 4: Rui P. Martins Analog and Mixed-Signal CMOS Circuits: <i>The Emergence and Leadership of a Lab, a Reference Book and the Future at the Core of the A/D Interface in the IoE</i></p>					
15:40 - 15:50	changing room					
15:50 - 17:10	C6L-1 Optoelectronic Device Modeling	C6L-2 Memory Design & In-Memory Computing	C6L-3 ADC & RF Interface	C6L-4 mm-Wave Transceivers	C6L-5 Power & RF Devices: From Substrate to Packaging	
17:10 - 17:50	FAREWELL COFFEE BREAK					